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Intel® JHL6340 Thunderbolt™ 3 Controller

Specifications Essentials I/O Specifications **Package Specifications**

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Specifications	
- Essentials	
Status	Launched
Launch Date	Q2'16
Embedded Options Available	No
TDP	1.7 W
Recommended Customer Price	
Datasheet	Link
Conflict Free	Yes
A 10 0 10 10	
- I/O Specifications	
# of Ports	Single
PCI Express Configurations ‡	Gen 3
Display Port	1.2
- Package Specifications	
Package Size	10.7x10.7mm
Low Halogen Options Available	See MDDS

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PCN/MDDS Information

SLLSP

950430: MDDS

SLLSQ

950431: PCN | MDDS

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Refer to Datasheet for formal definitions of product properties and features.

"Announced" SKUs are not yet available. Please refer to the Launch Date for market availability.

‡ This feature may not be available on all computing systems. Please check with the system vendor to determine if your system delivers this feature, or reference the system specifications (motherboard, processor, chipset, power supply, HDD, graphics controller, memory, BIOS, drivers, virtual machine monitor-VMM, platform software, and/or operating system) for feature compatibility. Functionality, performance, and other benefits of this feature may vary depending on system configuration.

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System and Maximum TDP is based on worst case scenarios. Actual TDP may be lower if not all I/Os for chipsets are used.

Low Halogen: Applies only to brominated and chlorinated flame retardants (BFRs/CFRs) and PVC in the final product. Intel components as well as purchased components on the finished assembly meet JS-709 requirements, and the PCB / substrate meet IEC 61249-2-21 requirements. The replacement of halogenated flame retardants and/or PVC may not be better for the environment.

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